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APPLICATION NO.	FII	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/020,741	10/020,741 12/12/2001		Charles H. Dennison	2269-3259.1US	2283
24247	7590	10/21/2003	•	EXAMINER	
TRASK BE	TTL		NGUYEN, TUAN H		
P.O. BOX 2:	550				
SALT LAKE CITY, UT 84110				· ART UNIT	PAPER NUMBER
				2813	

DATE MAILED: 10/21/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

• •	Application No.	Applicant(s)					
Office Action Comments	10/020,741	DENNISON ET AL.					
Offic Action Summary	Examiner	Art Unit					
	Tuan H. Nguyen	2813					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address P riod for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).  Status							
1) Responsive to communication(s) filed on 22 J	luly 2003 .						
2a)☐ This action is <b>FINAL</b> . 2b)⊠ Th	is action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayie, 1935 C.D. 11, 453 O.G. 213.  Disposition of Claims							
4) Claim(s) 61-71 is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>61-71</u> is/are rejected.							
7)☐ Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o	r election requirement.						
Application Papers							
9)☐ The specification is objected to by the Examine							
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.							
If approved, corrected drawings are required in reply to this Office action.							
12) The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120  13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) All b) Some * c) None of:							
a) ☐ All b) ☐ Some c) ☐ None of.  1. ☐ Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.							
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Inform	nary (PTO-413) Paper No(s) nal Patent Application (PTO-152)					

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#### **DETAILED ACTION**

The reissue oath/declaration filed with this application is defective because it fails to contain a statement that all errors which are being corrected in the reissue application up to the time of filing of the oath/declaration arose without any deceptive intention on the part of the applicant. See 37 CFR 1.175 and MPEP § 1414.

In accordance with 37 CFR 1.175(b)(1), a supplemental reissue oath/declaration under 37 CFR 1.175(b)(1) must be received before this reissue application can be allowed.

Claims 61-71 are rejected as being based upon a defective reissue declaration under 35 U.S.C. 251. See 37 CFR 1.175. The nature of the defect is set forth above.

Receipt of an appropriate supplemental oath/declaration under 37 CFR

1.175(b)(1) will overcome this rejection under 35 U.S.C. 251. An example of acceptable language to be used in the supplemental oath/declaration is as follows:

"Every error in the patent which was corrected in the present reissue application, and is not covered by a prior oath/declaration submitted in this application, arose without any deceptive intention on the part of the applicant."

### **Double Patenting**

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double

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patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 61-71 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-66 of U.S. Patent No. RE38,049. Although the conflicting claims are not identical, they are not patentably distinct from each other because they are both directed to a method for fabricating a capacitor including the steps of forming a conformal first conductive layer over the first insulating layer and in a container formed in the first insulating layer; forming a second insulating layer above the first conductive layer; and removing a portion of the second insulating layer through the use of CMP until an upper portion of the first conductive layer is exposed.

## Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 61-65 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 61, line 3, "said insulating layer" lacks antecedent basis.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 61-71 are rejected under 35 U.S.C. 102(e) as being anticipated by Gonzalez et al.(cited by applicant).

Gonzalez et al., figs. 1-15 and related text on col. 5-9 discloses the claimed method for forming a capacitor including the steps of providing a first insulating 40 of BPSG having a first etch rate, and forming an opening 55 into the first insulating layer 40 thereby forming a container on the substrate 3 (fig. 3, col. 6, fifth paragraph); forming a generally conformal first conductive layer 60 over the first insulating layer 40 and in the opening 55 of the container; forming a second insulating layer 65 of oxide having a second etch rate, above the first conductive layer 60 (fig. 4, col. 6, sixth paragraph); removing at least a portion of the second insulating 65 through the use of CMP until an upper portion of the first conductive layer 60 is exposed; and removing at least a portion of the first conductive layer 60 until the first insulating 40 is exposed, thereby forming a conductive container having inner and outer walls (fig. 7B, 8B, col. 7, fifth paragraph).

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### Claim R jections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 61-71 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kwon et al. in view of Gonzalez et al. (both cited by applicant).

Kwon et al., figs. 2-4 and text on col. 2-3 discloses substantially the claimed process for forming a capacitor including the steps of forming a conformal first conductive layer 48 over the first insulating layer 46 of oxide having a first etch rate and in the container 54; forming a second insulating layer 50 of photoresist having a second etch rate over the first conductive layer 48; removing the second insulating layer 50 until an upper portion of the first conductive layer 48 is exposed (fig. 4C); removing at least a portion of the upper portion of the first conductive layer 48 until the first insulating layer 46 is exposed, thereby forming a conductive container having inner and outer walls (fig. 4D).

Kwon et al., fig. 4C and text on col. 3, lines 21-25 generally discloses the use of etch-back process for planarizing the second insulating layer until the first conductive layer 48 is exposed. Kwon et al. does not particularly teach the use of CMP for etch-back process.

Gonzalez et al., in a related method for forming a capacitor as shown particularly in figs. 7A, 7B and text on col. 7, fourth and fifth paragraphs, teaches the use of either

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wet etching or chemical mechanical planarization method for etching back the layers for forming a conductive container having inner and outer walls.

Accordingly, it would have been obvious to one having ordinary skill in the art at the time the invention was made to have used CMP as an alternative method as suggested by Gonzalez et al. in the etch-back process of Kwon et al. for planarizing the semiconductor surface, since they are well-known alternative methods for planarizing the semiconductor surface in the semiconductor art.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan H. Nguyen whose telephone number is 703-308-2550. The examiner can normally be reached on 9AM-5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead Jr. can be reached on 703-308-4940. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Primary Examiner

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